

OVERALL MECHANICAL		
1	NUMBER OF LAYERS	4
2	BOARD THICKNESS (mm)	1.60
3	PCB WIDTH (mm)	73.50
4	PCB LENGTH (mm)	110.00

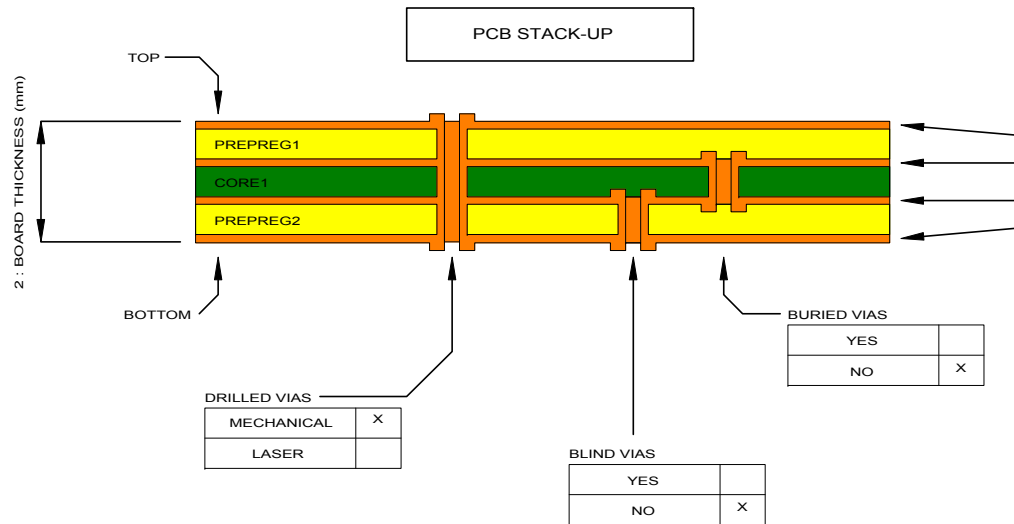
REFER TO "VIEW 18" FOR DETAILED DIMENSION & SHAPE

MATERIAL		
5	BASE MATERIAL TYPE	FR4
6	OUTER LAYER COPPER FOIL	1oz(35um)
7	INNER LAYER COPPER FOIL	1oz(35um)
8	CORE1 THICKNESS	MFG*
9	PREPREG1 THICKNESS	MFG
10	PREPREG2 THICKNESS	MFG

\*MFG : MANUFACTURER WILL DECIDE THEIR STANDARD CORE/PREGREG THICKNESS TO MEET REQUIRED FINISHED THICKNESS

TECHNOLOGY			
11	MINIMUM TRACE WIDTH (mm)	0.15	
12	MINIMUM SPACING (mm)	0.15	
13	MINIMUM DRILLED PLATED HOLE (mm)	0.30	
14	SOLDERMASK	2	COLOUR BLUE
15	LEGEND/SILK	2	COLOUR WHITE
16	SURFACE FINISH	ENIG	
17	PATTERN CLASS	6	
18	PCB RATIO (THICKNESS/DRILL DIAMETER)	N/A	
19	CHAMFER EDGE	NO	

REFER TO "VIEW 18" FOR DETAILED DRILLING/DIMENSION PATTERN



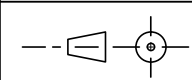
REFER TO "VIEW 18" FOR DETAILED DRILLING INFORMATION

LAYER DEFINITION		
20	COPPER LAYER 1	TCOPPER.GBR
21	COPPER LAYER 2	INNER1 : INNER1_GND.GBR
22	COPPER LAYER 3	INNER2 : INNER2_VCC.GBR
23	COPPER LAYER 4	BCOPPER.GBR

MANUFACTURING REQUIREMENTS		
24	PANELISATION	YES
25	BREAKOUT PIPS	NO
26	VCUT	YES
27	TOOL AREA (BAR/FRAME)	YES
28	TOOLING HOLE/FIDUCIALS	YES
29	TESTING	YES

REFER TO "VIEW 18" FOR PANEL VISUAL REQUIREMENTS/RESTRICTIONS

REMARKS	
30	
31	
32	
33	

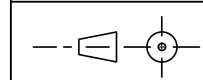
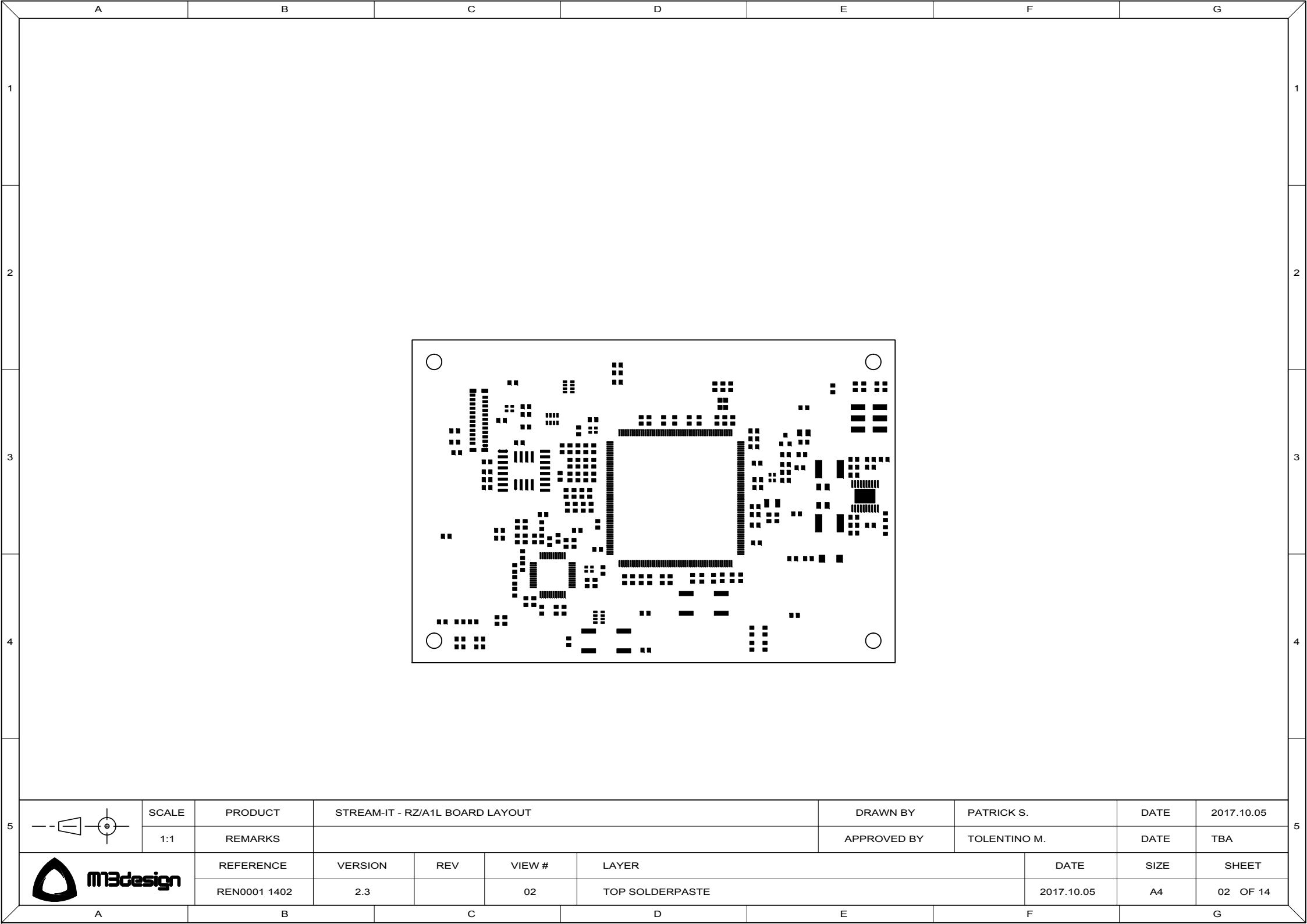



SCALE  
1:1

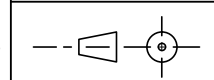
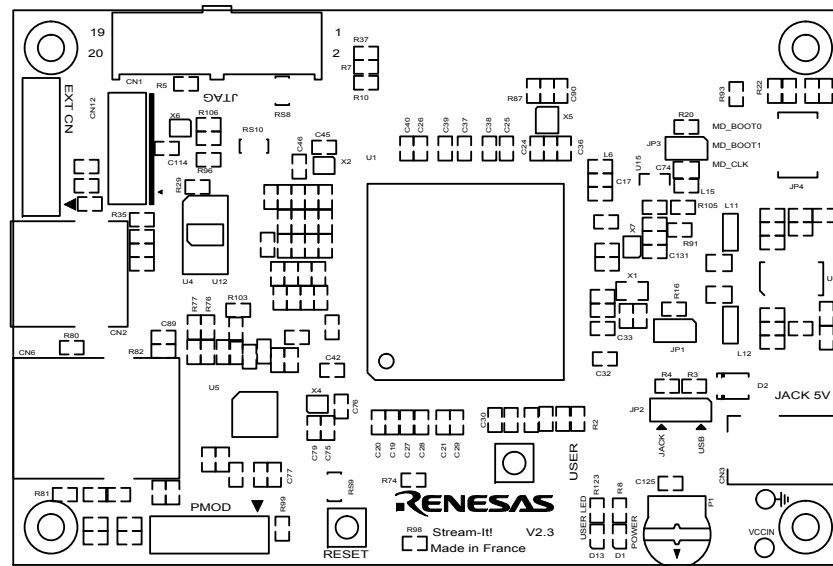
PRODUCT	STREAM-IT - RZ/A1L BOARD LAYOUT				DRAWN BY	PATRICK S.	DATE	2017.10.05
REMARKS					APPROVED BY	TOLENTINO M.	DATE	TBA



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		01	PCB SPECIFICATION	2017.10.05	A4	01 OF 14



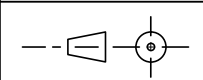
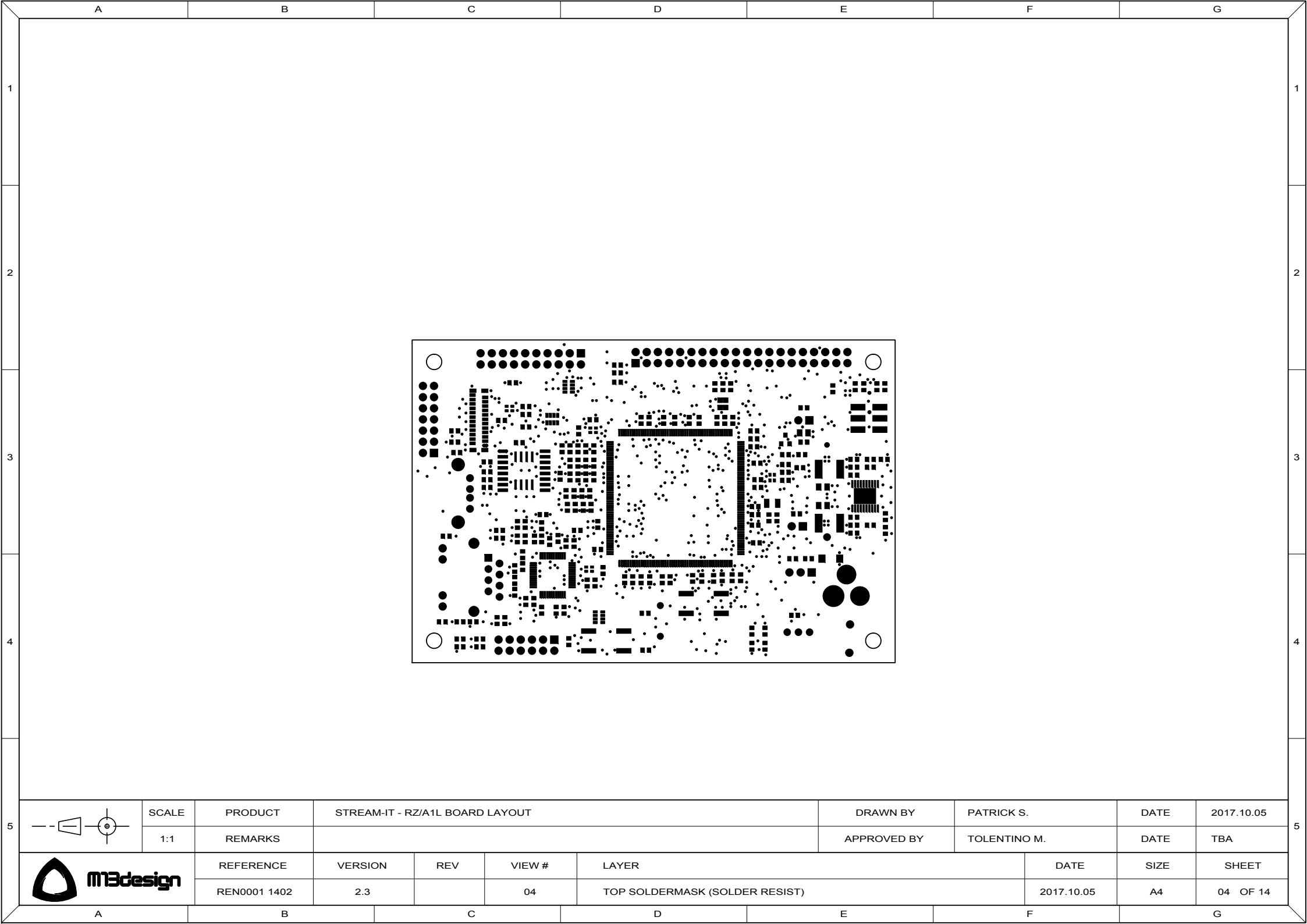
SCALE	PRODUCT					DRAWN BY		DATE			
	1:1	STREAM-IT - RZ/A1L BOARD LAYOUT					PATRICK S.		2017.10.05		
REMARKS							APPROVED BY		DATE		
							TOLENTINO M.		TBA		
		REFERENCE	VERSION	REV	VIEW #	LAYER			DATE	SIZE	SHEET
		REN0001 1402	2.3		02	TOP SOLDERPASTE			2017.10.05	A4	02 OF 14



SCALE	PRODUCT				STREAM-IT - RZ/A1L BOARD LAYOUT		DRAWN BY	PATRICK S.	DATE	2017.10.05
	1:1	REMARKS						APPROVED BY	TOLENTINO M.	DATE



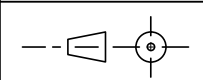
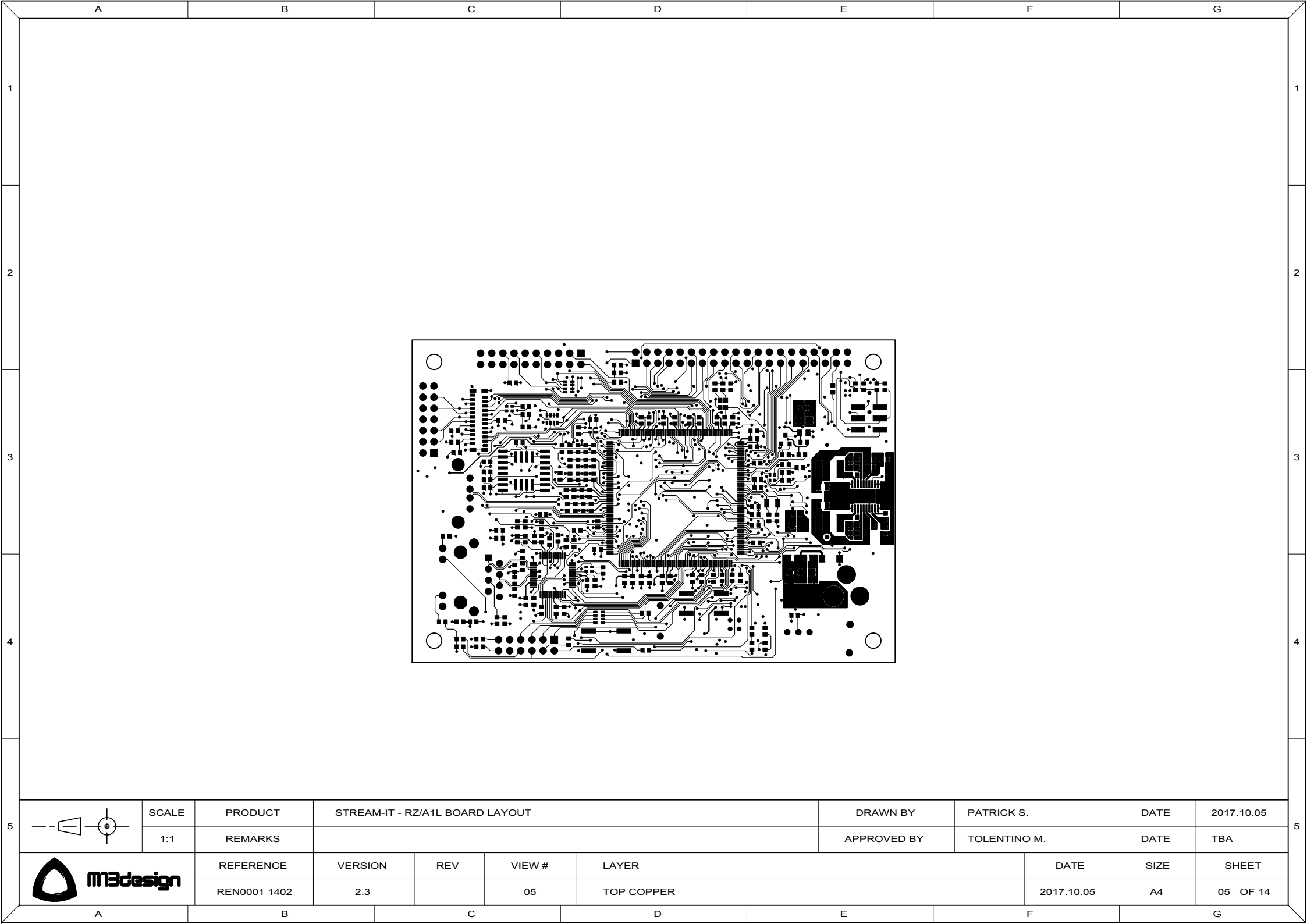
REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		03	TOP SILKSCREEN	2017.10.05	A4	03 OF 14



SCALE	PRODUCT				DRAWN BY		DATE				
	1:1	REMARKS				APPROVED BY		DATE			
REFERENCE		VERSION	REV	VIEW #	LAYER		DATE	SIZE	SHEET		
REN0001 1402		2.3		04	TOP SOLDERMASK (SOLDER RESIST)		2017.10.05	A4	04 OF 14		
PRODUCT		STREAM-IT - RZ/A1L BOARD LAYOUT				DRAWN BY		PATRICK S.		DATE	2017.10.05
LAYER						APPROVED BY		TOLENTINO M.		DATE	TBA



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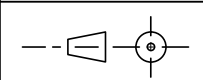
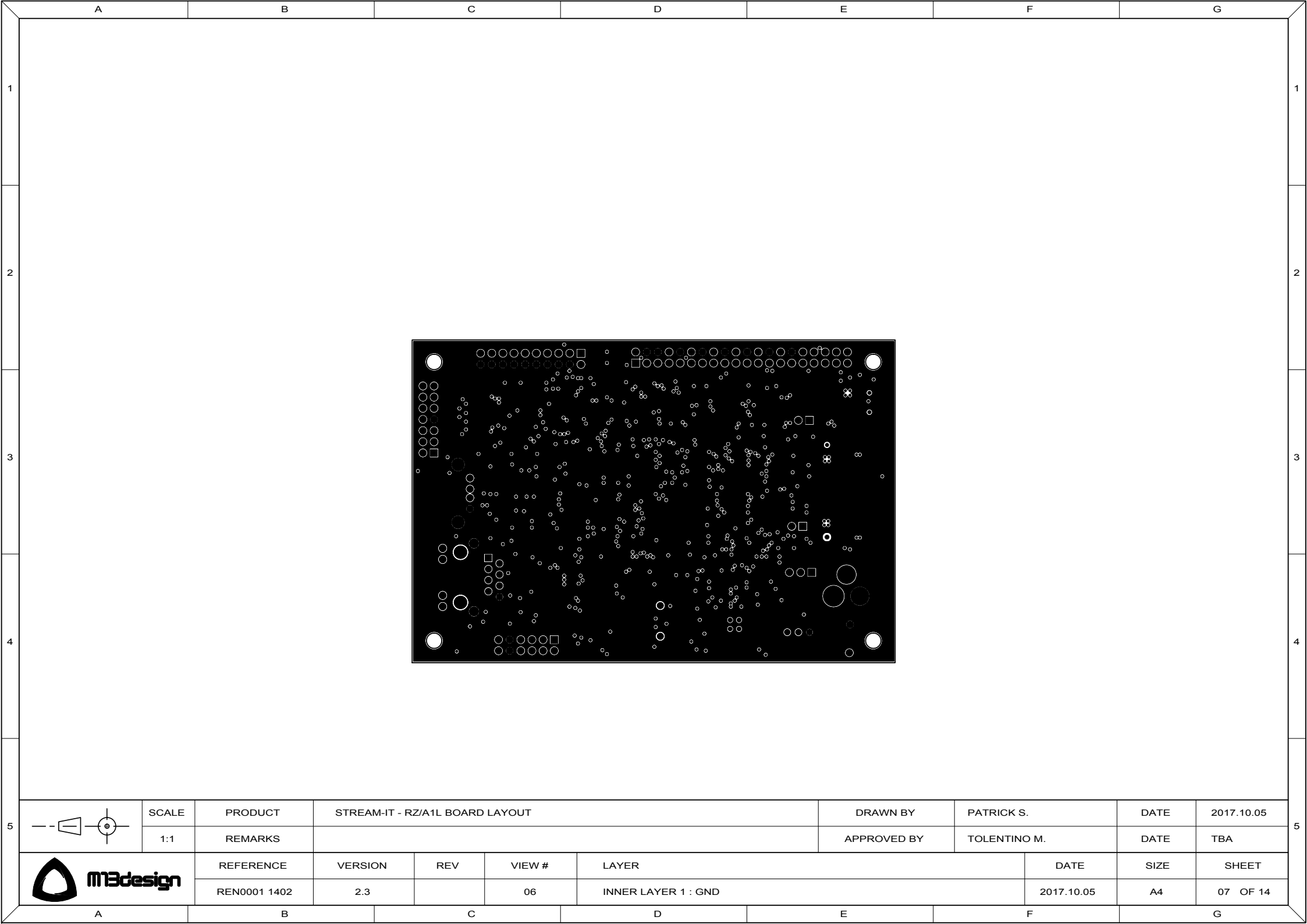



SCALE	PRODUCT				DRAWN BY		DATE	2017.10.05	
	1:1	REMARKS				APPROVED BY		DATE	TBA



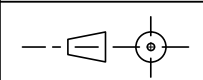
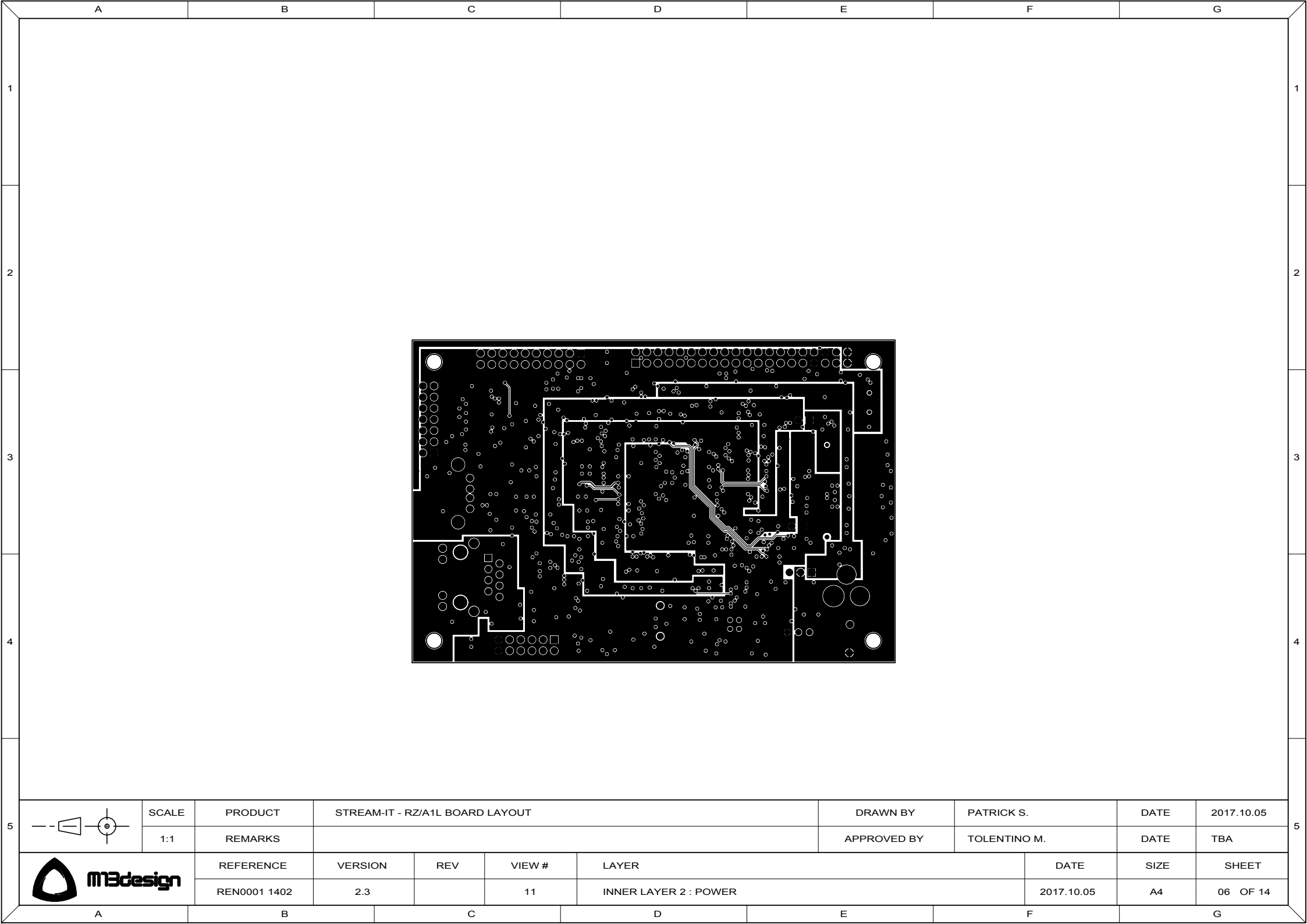
REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		05	TOP COPPER	2017.10.05	A4	05 OF 14

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SCALE	PRODUCT					DRAWN BY	PATRICK S.	DATE	2017.10.05	
	1:1	REMARKS					APPROVED BY	TOLENTINO M.	DATE	TBA
		REFERENCE	VERSION	REV	VIEW #	LAYER		DATE	SIZE	SHEET
		REN0001 1402	2.3		06	INNER LAYER 1 : GND		2017.10.05	A4	07 OF 14

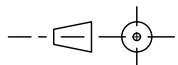
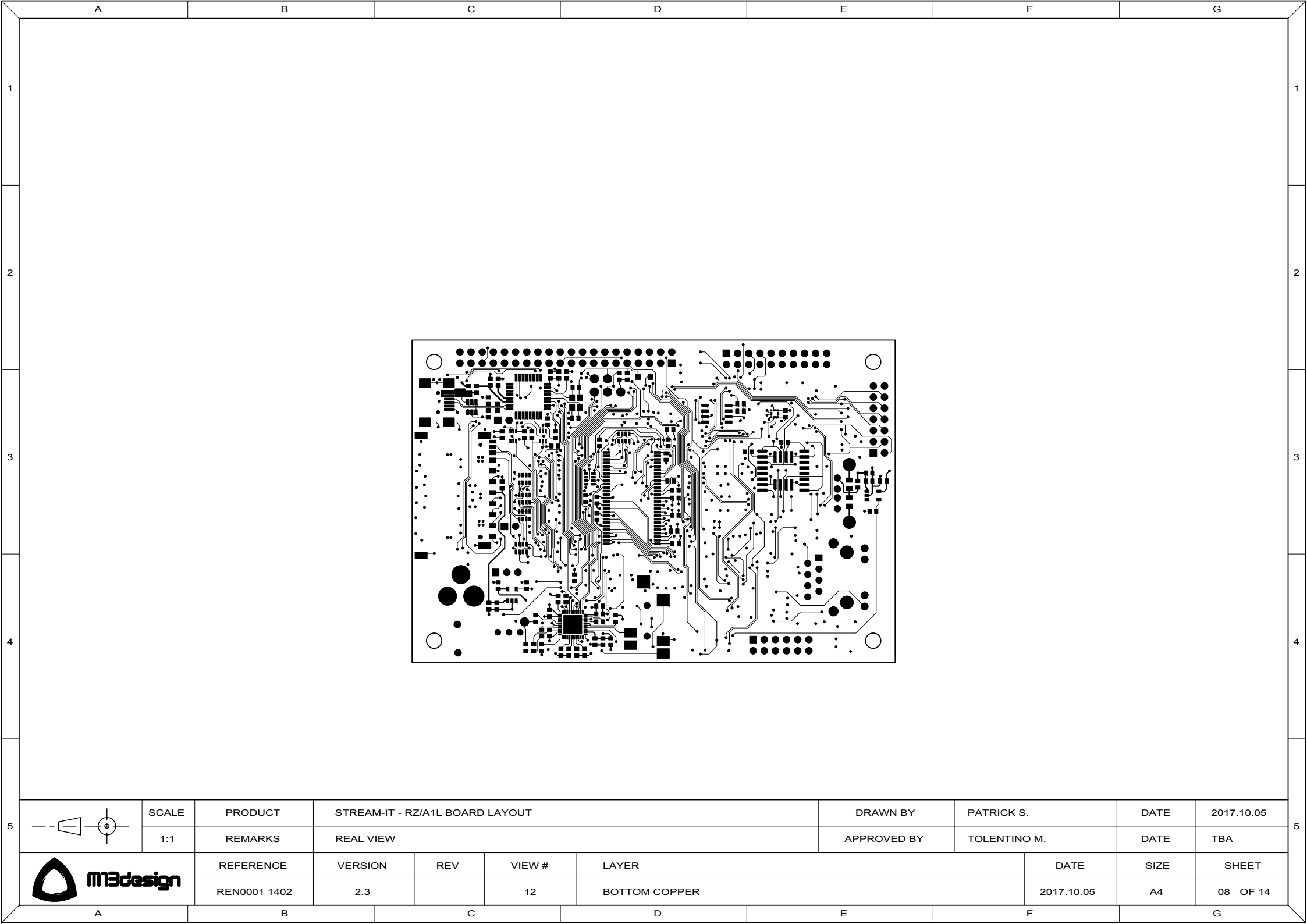
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SCALE	PRODUCT				DRAWN BY		DATE	2017.10.05	
	1:1	REMARKS				APPROVED BY		DATE	TBA



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		11	INNER LAYER 2 : POWER	2017.10.05	A4	06 OF 14



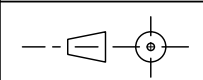
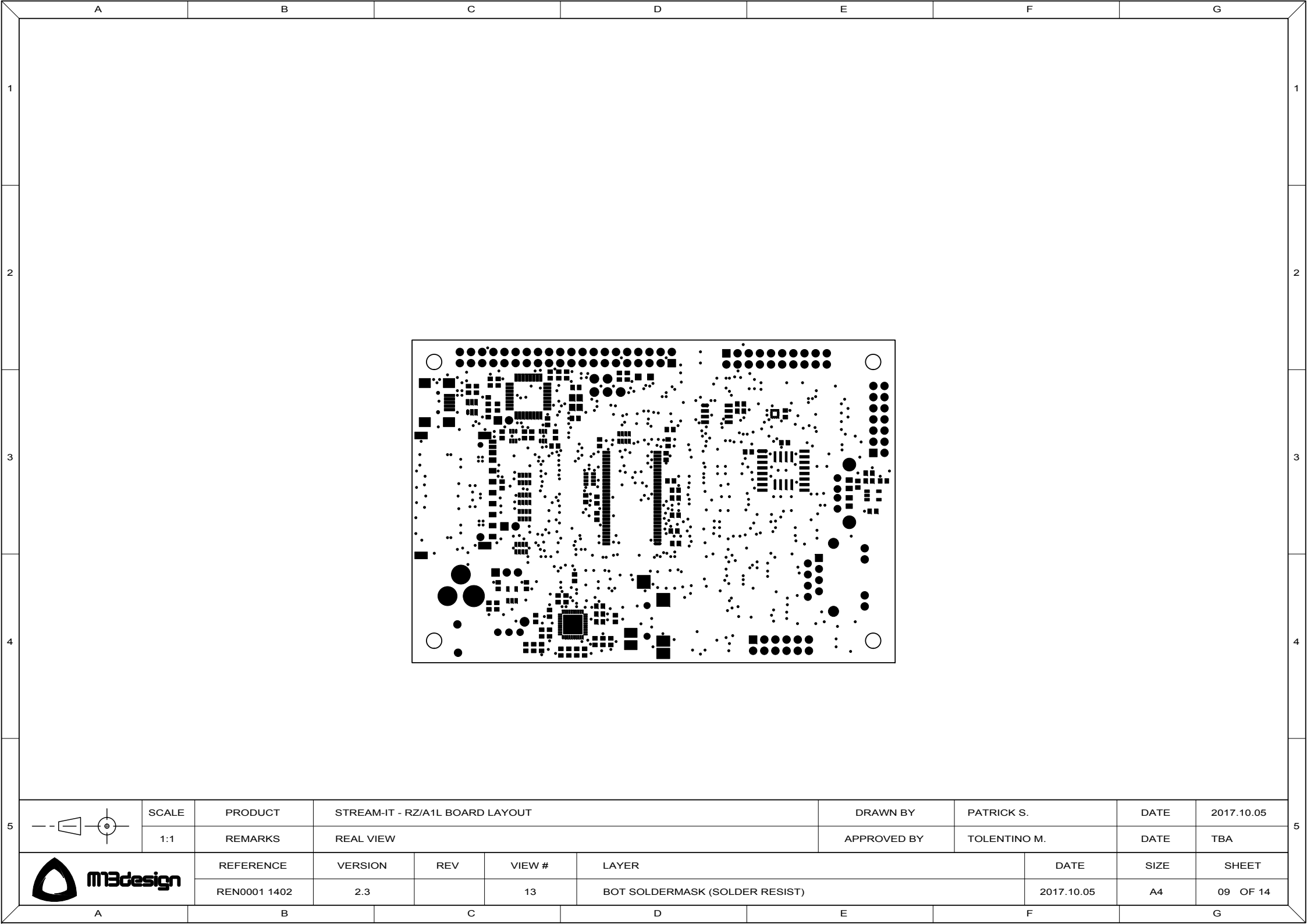
SCALE	PRODUCT				STREAM-IT - RZ/A1L BOARD LAYOUT		DRAWN BY	PATRICK S.	DATE	2017.10.05
	1:1	REMARKS		REAL VIEW		APPROVED BY	TOLENTINO M.	DATE	TBA	



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		12	BOTTOM COPPER	2017.10.05	A4	08 OF 14

A B C D E F G



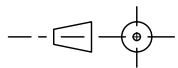
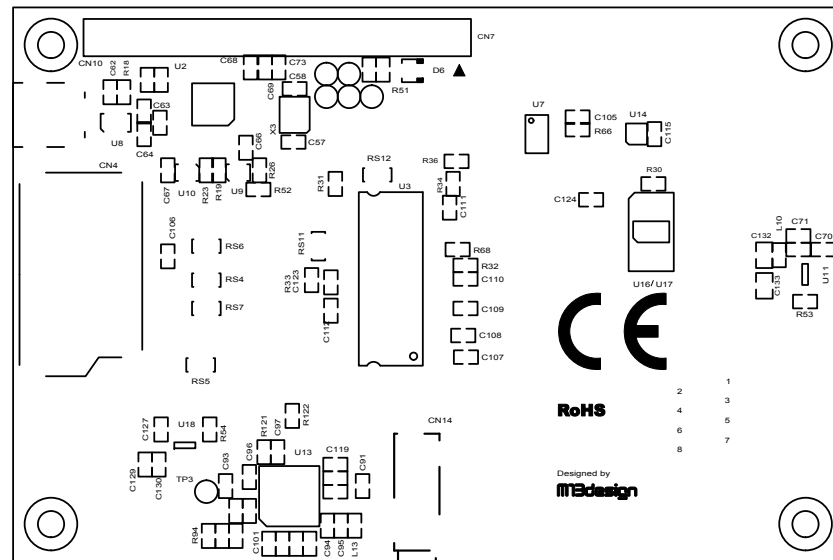


SCALE	PRODUCT				DRAWN BY		DATE		
	1:1	REMARKS				APPROVED BY		DATE	
PRODUCT		STREAM-IT - RZ/A1L BOARD LAYOUT				PATRICK S.		2017.10.05	
REMARKS		REAL VIEW				TOLENTINO M.		TBA	



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		13	BOT SOLDERMASK (SOLDER RESIST)	2017.10.05	A4	09 OF 14

A B C D E F G



SCALE  
1:1

PRODUCT  
REMARKS

STREAM-IT - RZ/A1L BOARD LAYOUT  
REAL VIEW

DRAWN BY  
APPROVED BY

PATRICK S.  
TOLENTINO M.

DATE  
DATE

2017.10.05  
TBA



REFERENCE  
REN0001 1402

VERSION  
2.3

REV

VIEW #  
14

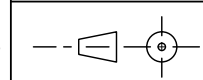
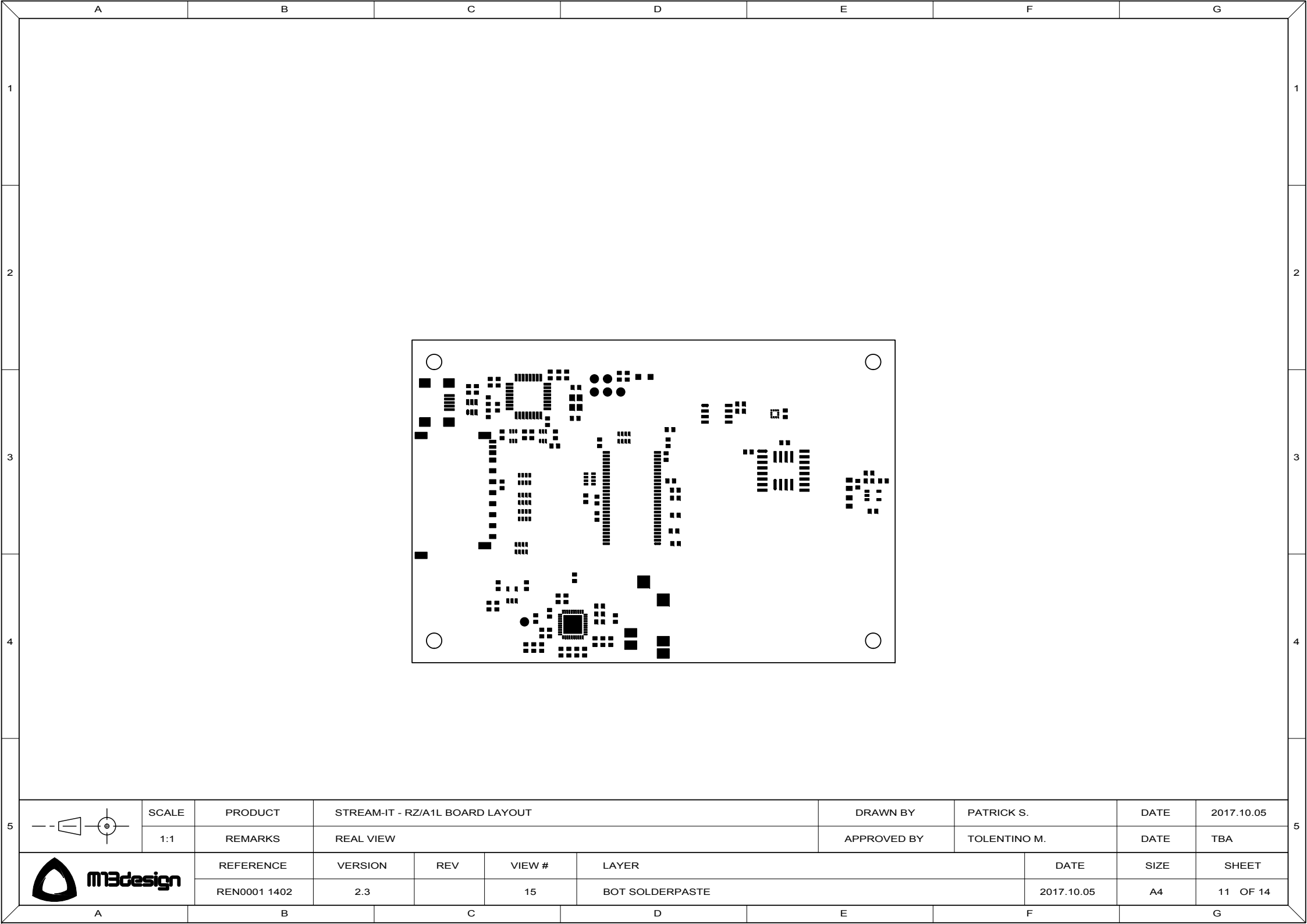
LAYER  
BOT SILKSCREEN

DATE  
2017.10.05

SIZE  
A4

SHEET  
10 OF 14

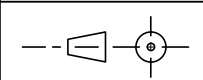
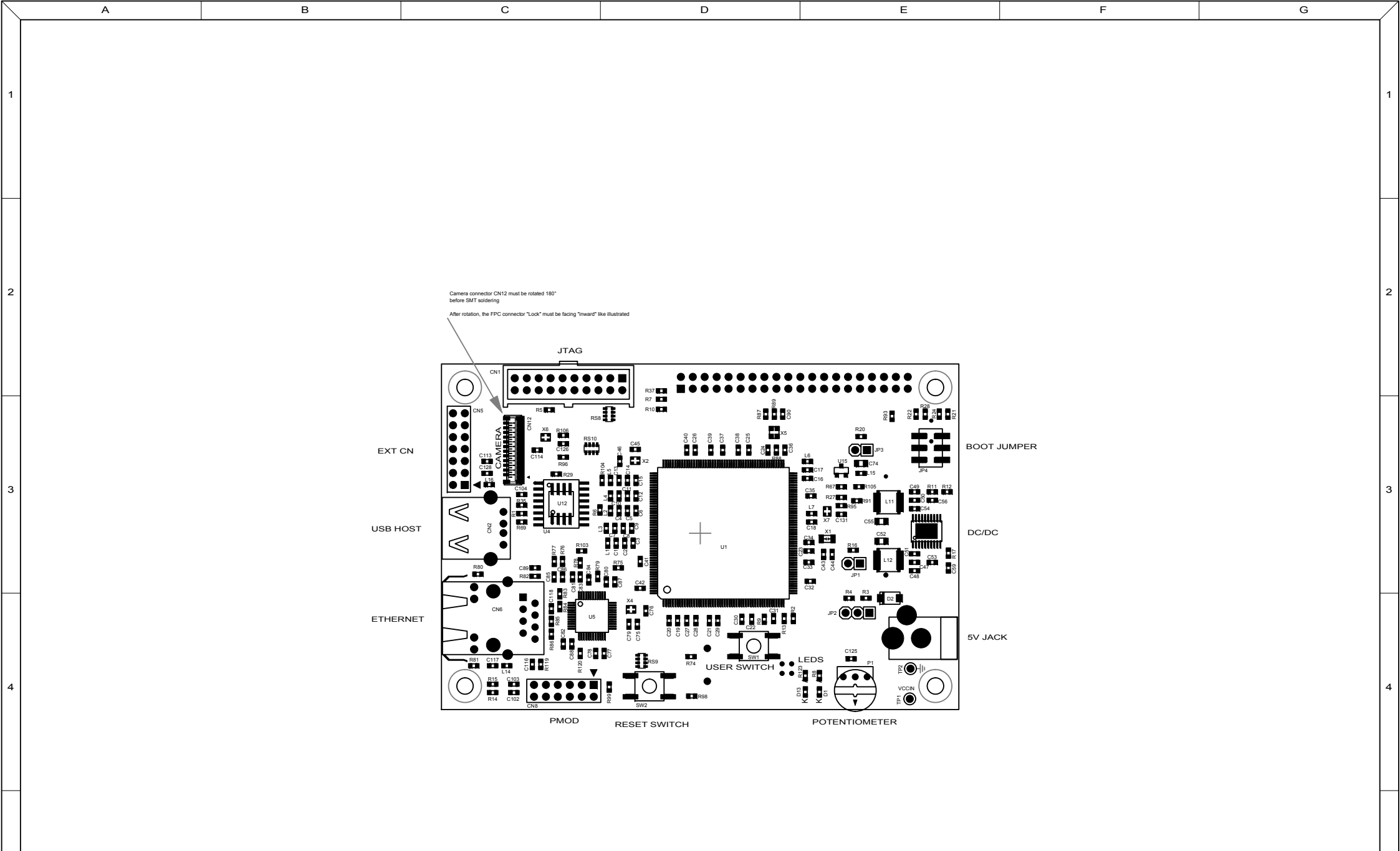
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SCALE	PRODUCT				DRAWN BY		DATE	2017.10.05		
	1:1	REMARKS				APPROVED BY		DATE	TBA	
REFERENCE		VERSION	REV	VIEW #	LAYER			DATE	SIZE	SHEET
REN0001 1402		2.3		15	BOT SOLDERPASTE			2017.10.05	A4	11 OF 14



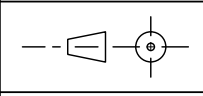
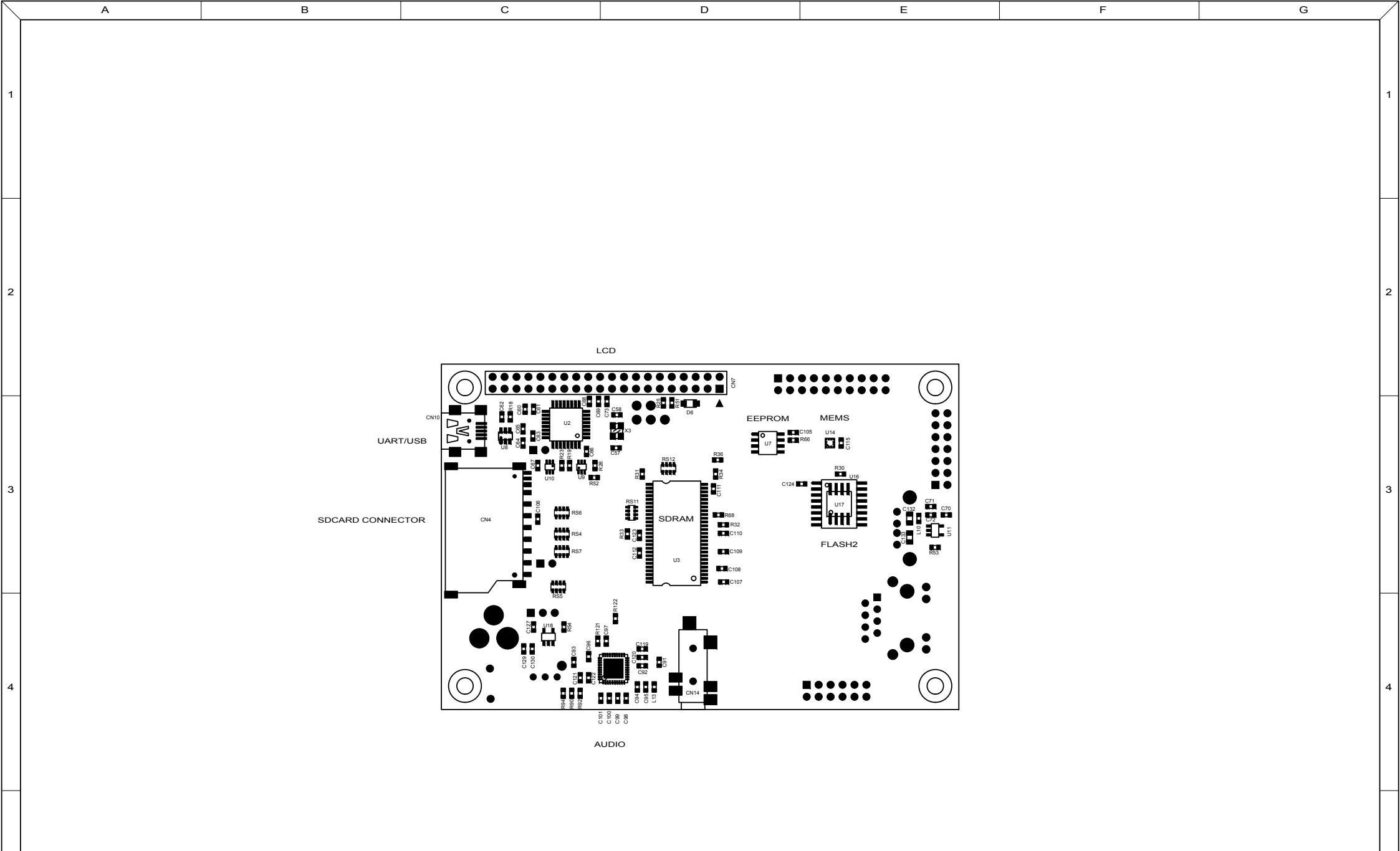
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SCALE	PRODUCT	STREAM-IT - RZ/A1L BOARD LAYOUT				DRAWN BY	PATRICK S.	DATE	2017.10.05
1:1	REMARKS					APPROVED BY	TOLENTINO M.	DATE	TBA



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		16	TOP LOCATION	2017.10.05	A4	12 OF 14



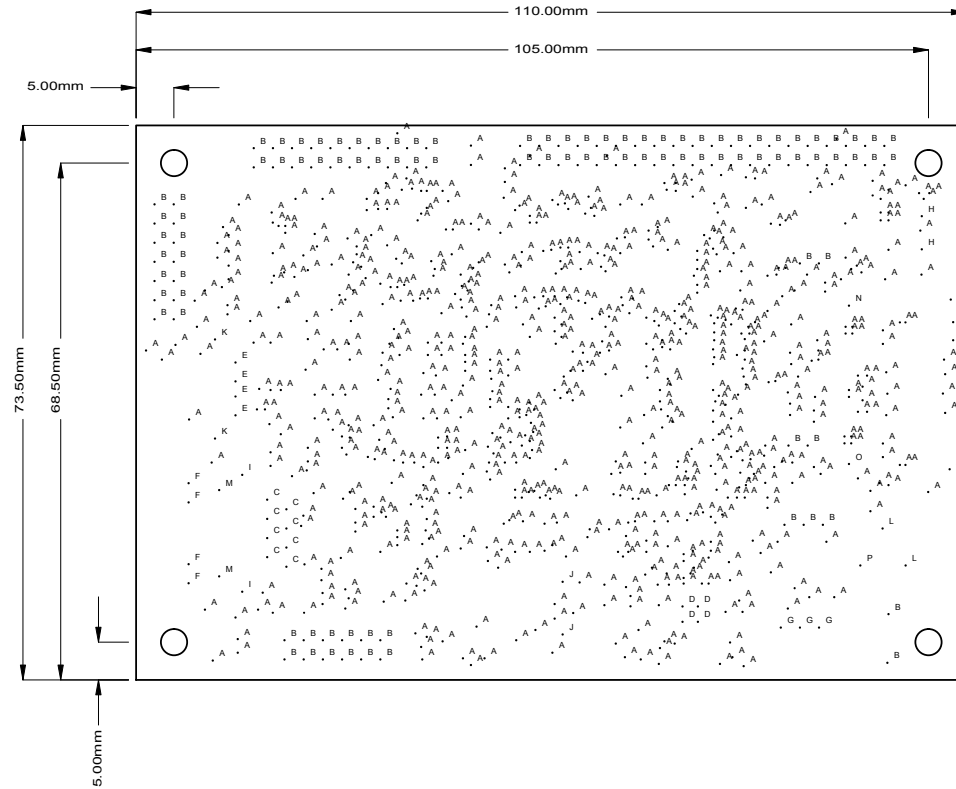
SCALE	PRODUCT				DRAWN BY		DATE	
	1:1	STREAM-IT - RZ/A1L BOARD LAYOUT				PATRICK S.		2017.10.05
REFERENCE	REMARKS				APPROVED BY		DATE	
	REN0001 1402	REAL VIEW				TOLENTINO M.		TBA



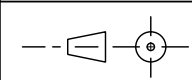
REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		17	BOTTOM LOCATION (REAL VIEW)	2017.10.05	A4	13 OF 14

Drill Table				
Size	Length	Plate Through	Letter	Count
0.300	0.000	YES	A	707
0.700	0.000	YES	D	4
0.800	0.000	YES	G	3
0.900	0.000	YES	C	8
0.900	0.000	NO	H	2
0.950	0.000	YES	E	4
1.000	0.000	YES	B	95
1.100	0.000	YES	F	4
1.100	0.000	NO	N	1
1.600	0.000	YES	I	2
1.600	0.000	NO	O	1
1.700	0.000	NO	J	2
2.300	0.000	YES	K	2
3.000	0.000	YES	L	2
3.200	0.000	NO	M	2
3.500	0.000	YES	P	1

Units=mm, Drill Optimisation=N,  
Slotted Holes=Y,  
Only Slotted Holes=N,  
Layer Pairs=(Through Hole),  
Include Equivalents=N



VERSION	REV.	DATE	DESCRIPTION
0.1		2014.09.17	CREATION
0.1	A	2014.09.24	FIRST DRAFT
0.1	B	2014.09.26	ALL WIRES CONNECTED
0.1	C	2014.09.30	POWER/GND PLANE ADDED, TOP/BOTTOM SILK UPDATED, RENESAS/M3DESIGN LOGO ADDED DRC 100% OK, DRILL TABLE UPDATED, REVISION TABLE ADDED
0.1	D	2014.10.01	LAYOUT MATCHES SCHEMATIC V0.1G
0.1	E	2014.10.02	RECTIFIED SILKSCREEN, ADDED DATE CODE AREA ON BOTTOM SILK, CORRECTED PASTE LAYERS CORRECTED MASK LAYERS, UPDATED TVS DIODE FOOTPRINT
0.2		2014.10.08	UPDATED ACCORDING TO SCHEMATIC V0.2, UPDATED TOP/BOT SILK, ADJUSTED POT/PMOD POSITION
1.0		2015.08.25	U1.103, C16 & C17 CONNECTED TO GND, REMOVED CE LOGO
2.0		2016.03.28	UPDATES FROM SCHEMATICS, USB HOST JACK CONNECTOR MOVED TO TOP LAYER ADDED CE LOGO
2.2		2016.12.07	UPDATES FROM SCHEMATICS V2.2 : VBUSIN1 / DM1 & DP1 CONNECTED TO USBDPVSS (GND), ADDED 22R DAMPING RESISTOR ON BOTH QSPI CLK, 100UF FILTER CAPACITOR ADDED ON USB HOST VBUS + AN EXTRA 0805 FOOTPRINT
2.3		2017.10.05	UPDATES FROM SCHEMATICS V2.3 : GND ON U10.3 & U9.3 / PULL-UP ON CKE, DQMLL, DQMLU / REMOVED VCC33 FROM U7.1 ADDED R34 BETWEEN U3.21 & U3.36 / ADDED R35 ON U16.7 / ADDED R36 IN LINE ON P3.14 / ADDED R37 PULLUP ON TRST



SCALE	PRODUCT	STREAM-IT - RZ/A1L BOARD LAYOUT				DRAWN BY	PATRICK S.	DATE	2017.10.05
1:1	REMARKS					APPROVED BY	TOLENTINO M.	DATE	TBA



REFERENCE	VERSION	REV	VIEW #	LAYER	DATE	SIZE	SHEET
REN0001 1402	2.3		18	DIMENSIONS	2017.10.05	A4	14 OF 14